Using PSPICE for Circuits with Dependent Sources

The problems in this tutorial and the corresponding homework continue to deal with the DC analysis of circuits, or DC Bias analysis in PSPICE. So far we have covered circuits with resistors and independent sources. This tutorial introduces the dependent source elements in PSPICE.

As discussed in class there are four types of dependent sources, two are voltage sources and two are current sources. The four types of dependent sources are listed below in table 1 along with their abbreviation, PSPICE name and library.

Table 1. Dependent Sources

<table>
<thead>
<tr>
<th>Dependent Source</th>
<th>Abbreviation</th>
<th>PSPICE Name</th>
<th>Library</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage Controlled Voltage Source</td>
<td>VCVS</td>
<td>E</td>
<td>Analog</td>
</tr>
<tr>
<td>Current Controlled Current Source</td>
<td>CCCS</td>
<td>F</td>
<td>Analog</td>
</tr>
<tr>
<td>Voltage Controlled Current Source</td>
<td>VCCS</td>
<td>G</td>
<td>Analog</td>
</tr>
<tr>
<td>Current Controlled Voltage Source</td>
<td>CCVS</td>
<td>H</td>
<td>Analog</td>
</tr>
</tbody>
</table>

Figure 1. Circuit with Dependent Source
Figure 1 above shows an example of a circuit with a current controlled voltage source. Open a new PSPICE project and enter this schematic. Note that the dependent source is found in the Place Part window as “H”. This is in the Analog library. Your completed schematic should be as shown in figure 2.

![PSPICE Schematic with Current Controlled Voltage Source](image)

Note that the right side of the device goes where the voltage source appears in the circuit. The left side is connected so that the controlling current (in this case $I_0$) runs through the left side of the dependent source symbol.

After the dependent source has been placed, left click on it to select it. Right-click on it to pop up a menu as shown in figure 3. Edit the property GAIN to set the gain (or constant) for the dependent source. Click the Apply button to set the gain and close this window.

![Property Editor for Dependent Source](image)

Run the DC bias analysis of this circuit as you have in the previous tutorials. The schematic shown in figure 4 should appear verifying the $I_0 = 3\text{A}$. 
Figure 4. Results of PSPICE Analysis