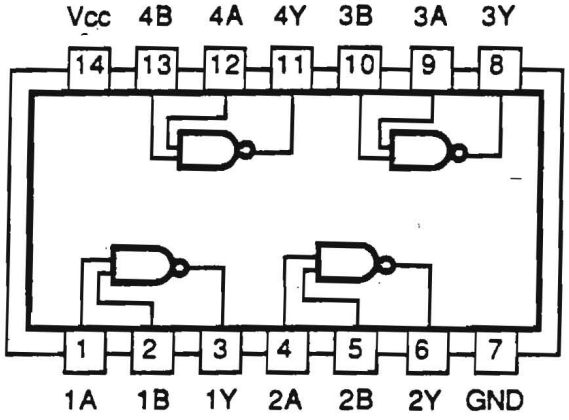
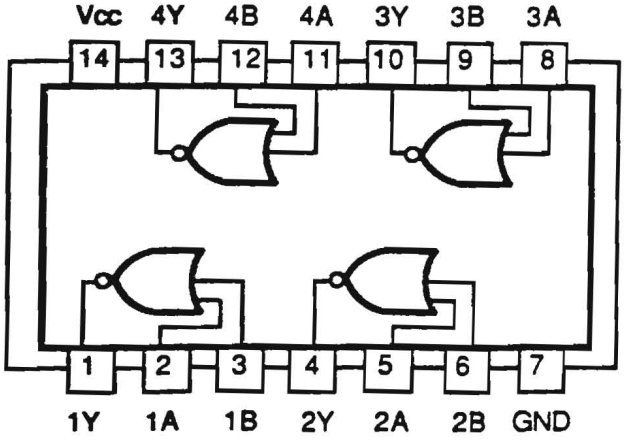
APPENDIX A

PINOUTS OF IC's USED IN CHAPTER 3



74LS00
Quadruple 2- Input
Positive NAND Gates
00
Positive Logic: $Y = \overline{AB}$



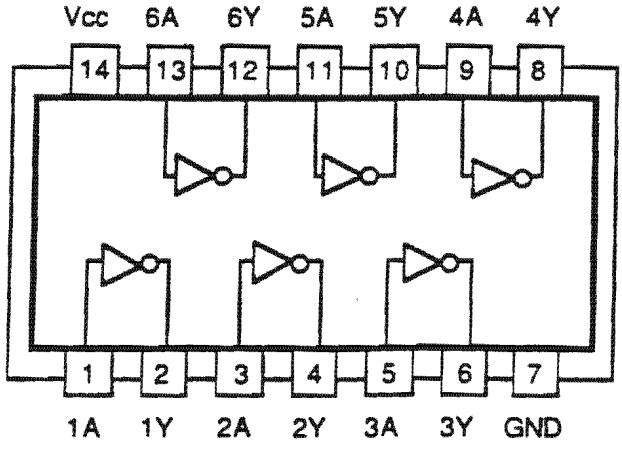
74LS02
Quadruple 2- Input
Positive NOR Gates
02
Positive Logic: $Y = \overline{A+B}$

74LS04

Hex Inverters

04

Positive Logic: $Y=\overline{A}$

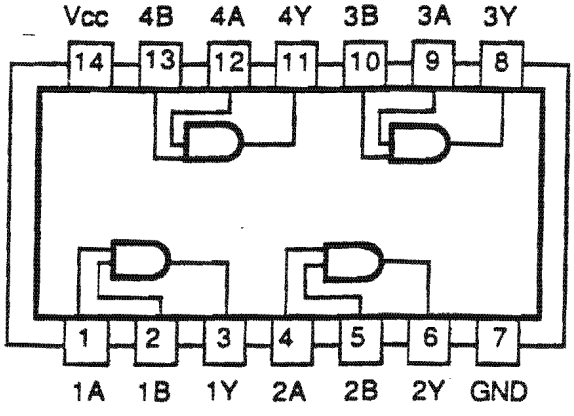


74LS08

Quadruple 2-Input
Positive AND Gates

08

Positive Logic:Y=AB

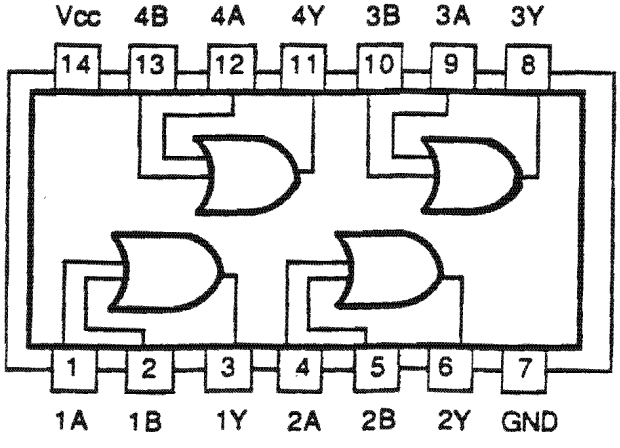


74LS32

Quadruple 2-Input
Positive OR Gates

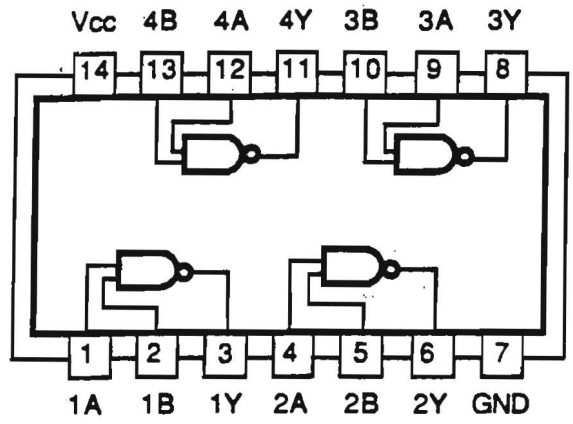
32

Positive Logic:Y=A+B

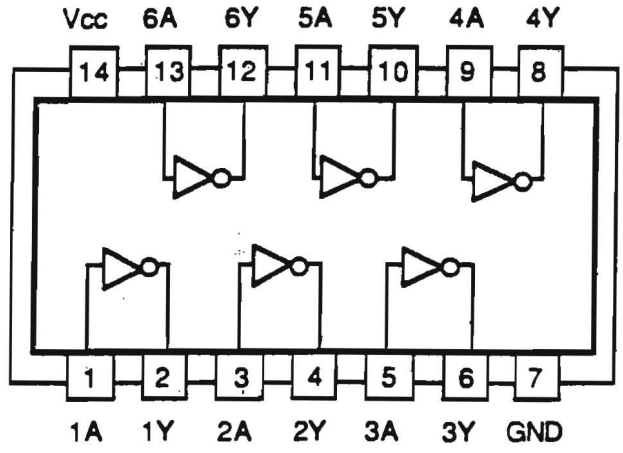


APPENDIX B

PINOUTS OF IC's USED IN CHAPTER 4

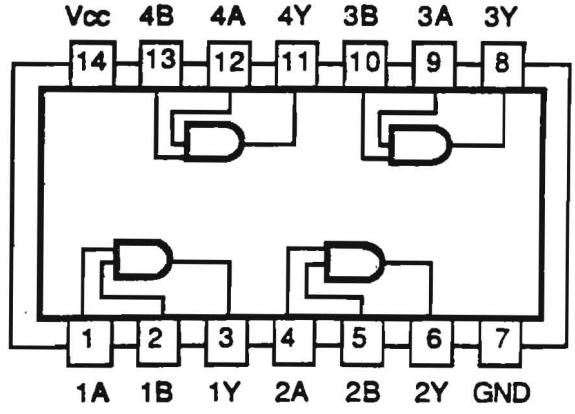


74LS00
Quadruple 2- Input
Positive NAND Gates
00
Positive Logic: $\overline{Y} = \overline{AB}$



74LS04

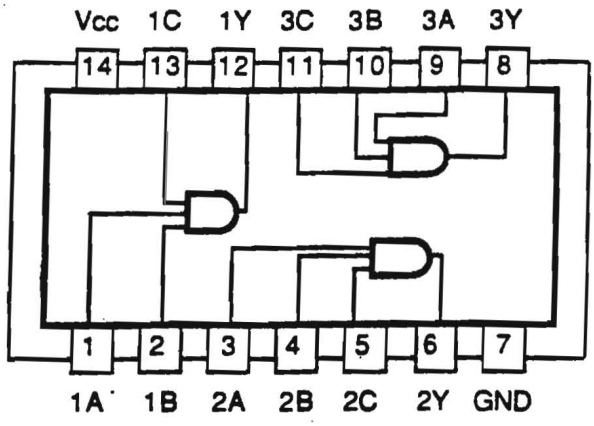
Hex Inverters
04
Positive Logic: $Y=\overline{A}$



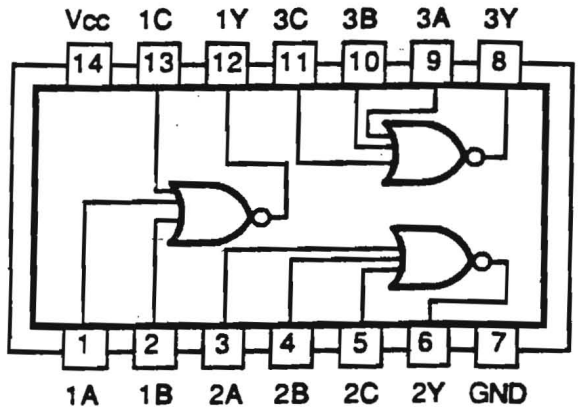
74LS08

Quadruple 2-Input
Positive AND Gates
08
Positive Logic:Y=AB

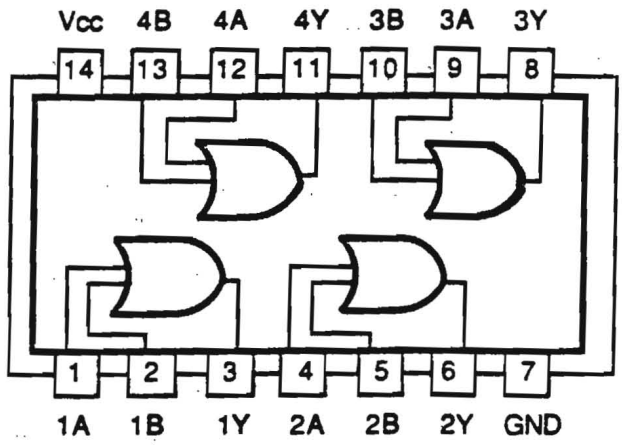
74LS11
Triple 3-Input
Positive AND Gates
11



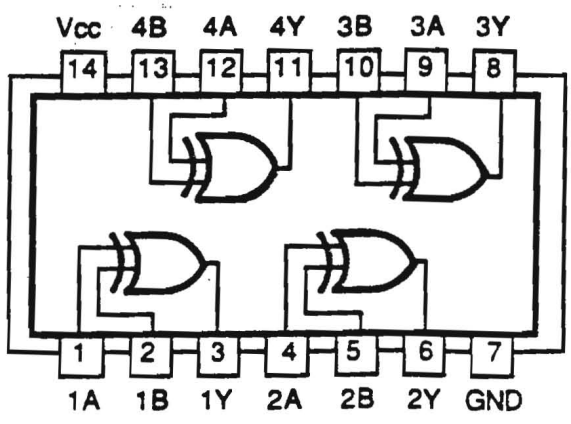
74LS27
Triple 3-Input
Positive NOR Gates
27



74LS32
Quadruple 2-Input
Positive OR Gates
32
Positive Logic:Y=A+B

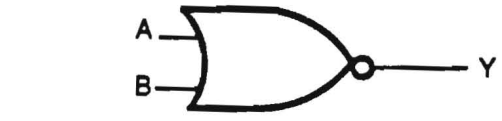


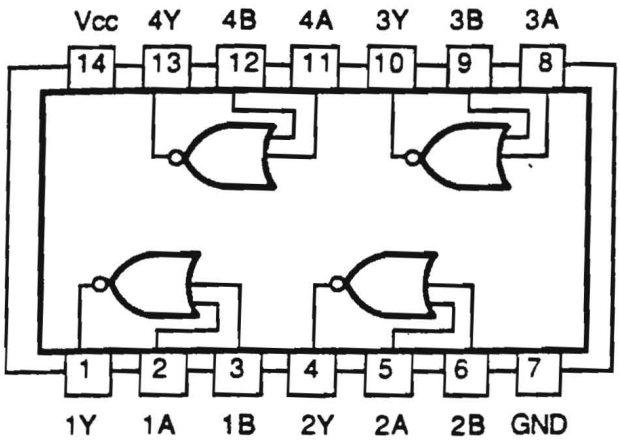
74LS86
Quadruple 2- Input
Exclusive OR Gates
86



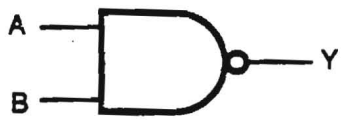
APPENDIX C

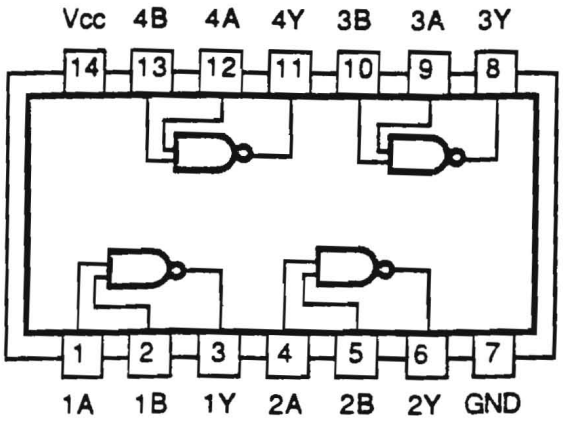
PINOUTS OF IC's USED IN CHAPTER 5





74LS02
Quadruple 2- Input
Positive NOR Gates
02
Positive Logic: $Y = \overline{A+B}$





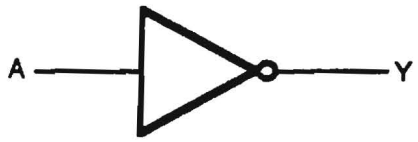
74LS00
Quadruple 2- Input
Positive NAND Gates
00
Positive Logic: $Y = \overline{AB}$

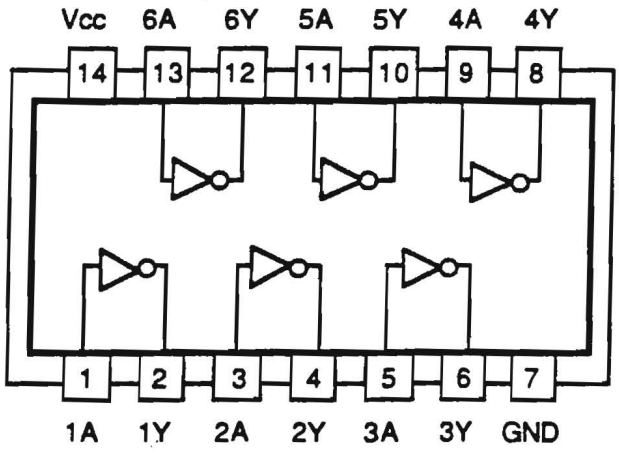
74LS04

Hex Inverters

04

Positive Logic: $Y=\overline{A}$





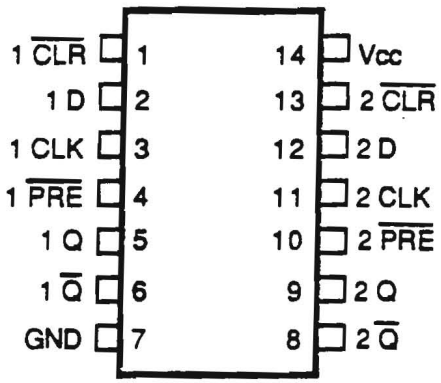
74SL74

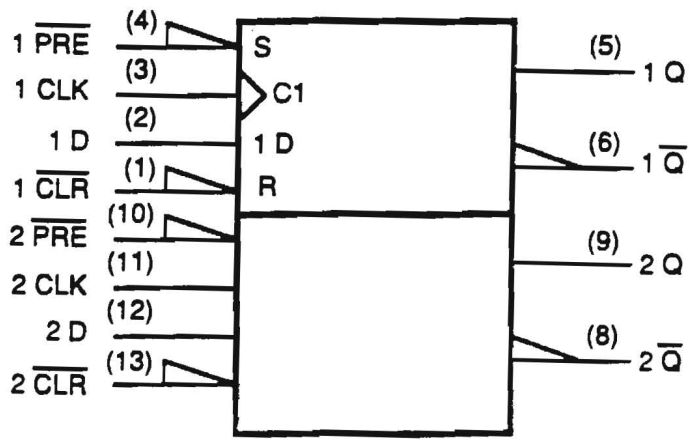
DUAL "D" Flip-Flop

Positive Edge Triggered

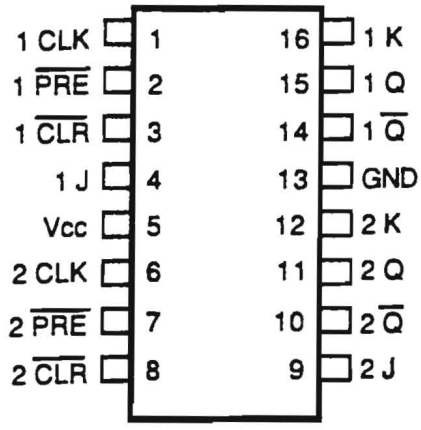
with Preset and Clear

(Top View)



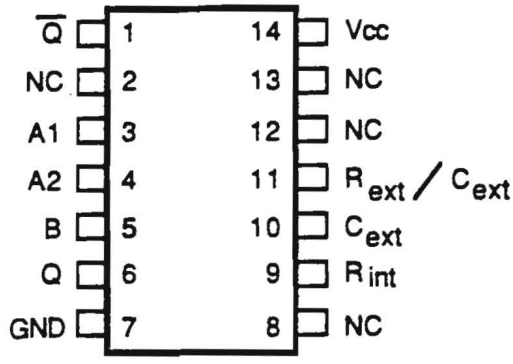


(Top View)



74SL76
DUAL "J-K" Flip-Flop
with Preset and Clear

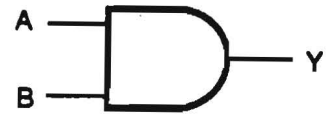
(Top View)

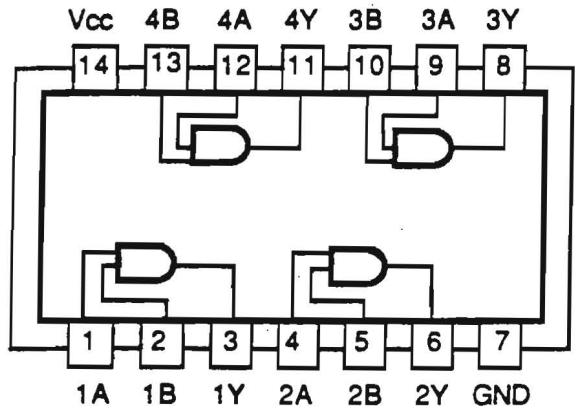


74121
Monostable Multivibrator
with Schmitt Trigger Inputs

APPENDIX D

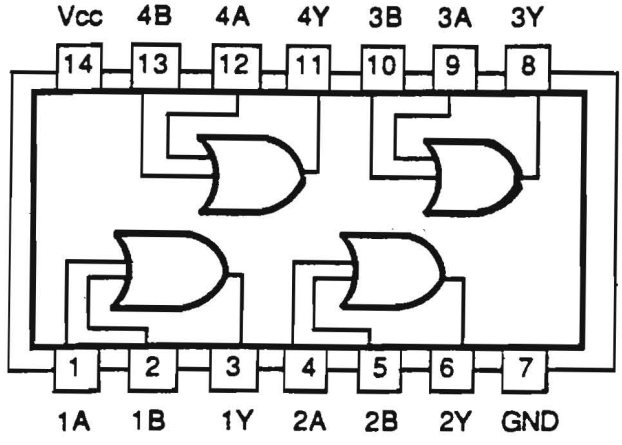
PINOUTS OF IC's USED IN CHAPTER 6





74LS08

Quadruple 2-Input
Positive AND Gates
08
Positive Logic:Y=AB

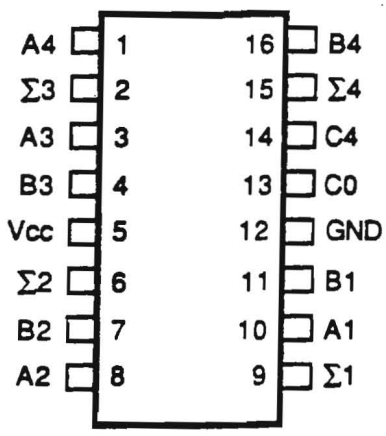


74LS32

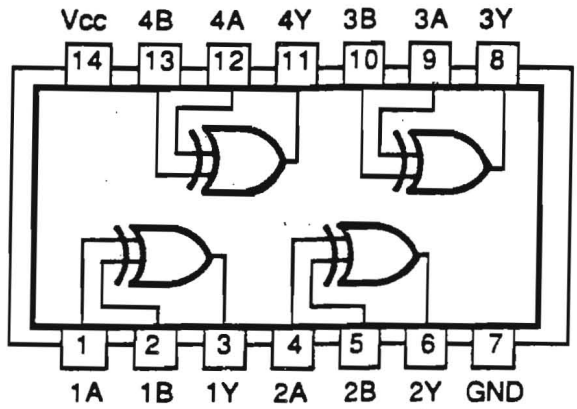
Quadruple 2-Input
Positive OR Gates
32
Positive Logic:Y=A+B

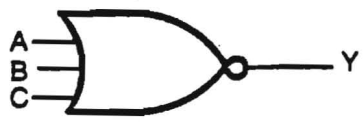
7483
4 - Bit Binary Full Adder
with Fast Carry

(Top View)

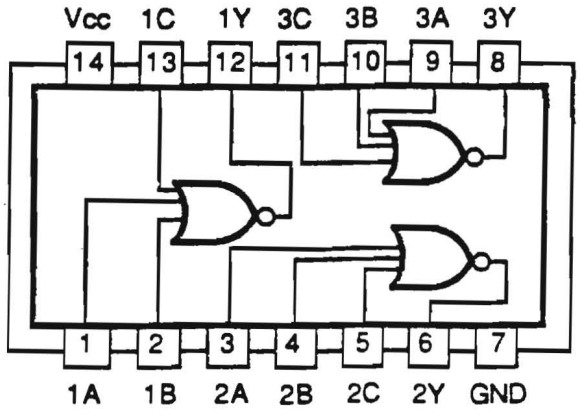


74LS86
Quadruple 2- Input
Exclusive OR Gates
86

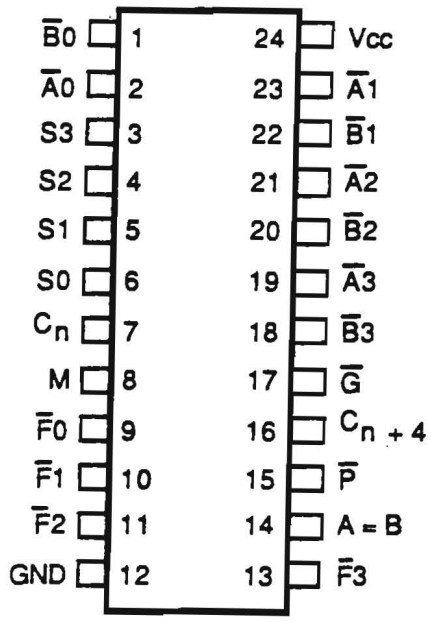




74LS27
Triple 3-Input
Positive NOR Gates
27

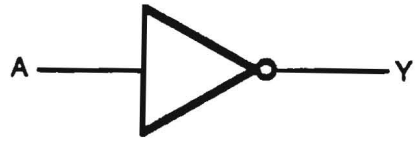


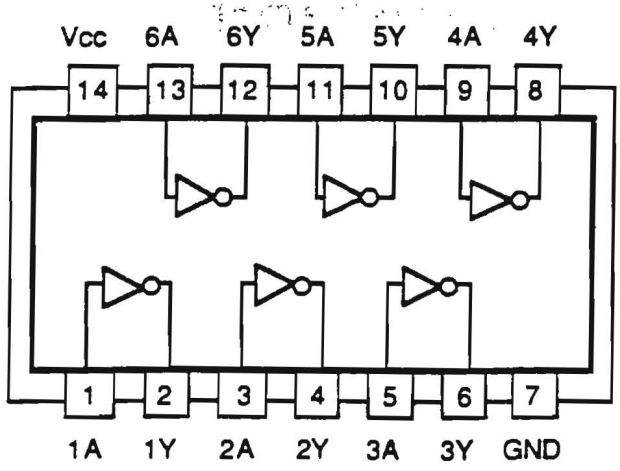
(Top View)



74181

Arithmetic Logic Unit





74LS04

Hex Inverters

04

Positive Logic: $Y=\overline{A}$